

Bit assignment of the RC memory word (32+6 bits) of the Heating RX radar controller

Bit N:o	Signal	Direction	Description	Default		Destination
	LPRPint	Not programmable				
H5	61	PSAV1	O, 0-act	Power-save TX1/2 ON	1	TX power-save logic
H4	60	PSAV2	O, 0-act	Power-save TX3/4 ON	1	TX power-save logic
H3	59	PSAV3	O, 0-act	Power-save TX5/6 ON	1	TX power-save logic
H2	58	PSAV4	O, 0-act	Power-save TX7/8 ON	1	TX power-save logic
H1	57	PSAV5	O, 0-act	Power-save TX9/10 ON	1	TX power-save logic
H0	56	PSAV6	O, 0-act	Power-save TX11/12 ON	1	TX power-save logic
	31	RXSYNC			0	Front panel PNC
	30	NCO-reset	O, neg. pulse	Zeroes NCO phase accum	1	Channel boards
	29	NCO-load	O, pos. pulse	Strobe selected freq	0	Channel boards
	28	NCO-select -9	Output	NCO freq. select bit 9	0	Channel boards
	27	NCO-select -8	Output	NCO freq. select bit 8	0	Channel boards
	26	NCO-select -7	Output	NCO freq. select bit 7	0	Channel boards
	25	NCO-select -6	Output	NCO freq. select bit 6	0	Channel boards
	24	NCO-select -5	Output	NCO freq. select bit 5	0	Channel boards
	23	NCO-select -4	Output	NCO freq. select bit 4	0	Channel boards
	22	NCO-select -3	Output	NCO freq. select bit 3	0	Channel boards
	21	NCO-select -2	Output	NCO freq. select bit 2	0	Channel boards
	20	NCO-select -1	Output	NCO freq. select bit 1	0	Channel boards
	19	NCO-select -0	Output	NCO freq. select bit 0	0	Channel boards
	18	ADSEL_1	O, level +/-	Selects ADC 1/2 for chan 2	1	Channel board 2
	17	BUFFLIP	O, neg. pulse	Flip buffer memory sides	1	Channel boards
	16	STFIR	O, neg. pulse	Start pulse for FIR filters	1	Channel boards
	15	CHON 6	O, neg. active	Dis/en writing into the BM 6	1	Channel board 6
	14	CHON 5	O, neg. active	Dis/en writing into the BM 5	1	Channel board 5
	13	CHON 4	O, neg. active	Dis/en writing into the BM 4	1	Channel board 4
	12	CHON 3	O, neg. active	Dis/en writing into the BM 3	1	Channel board 3
	11	CHON 2	O, neg. active	Dis/ena writes to the BM 2	1	Channel board 2
	10	CHON 1	O, neg. active	Dis/ena writes to the BM 1	1	Channel board 1
	9	ADSEL_0	O, level +/-	Selects ADC 1 2 for chan 1	1	Channel board 1
	8	INT	O, pos. pulse	Interrupt to DSP	0	CH-adapter board
	7	AD_strobe	O, neg. pulse	Strobe of ADC select reg	1	Channel boards
	6	RXP2B	O, ?	Receiver protector bit B	1	RX-protector 2B
	5	RXP2A	O, ?	Receiver protector bit A	1	RX-protector 2A
	4	Spare			0	
	3	S3	Output	Stat bit 3 to DSP Interf boards	0	CH-adapter board
	2	S2	Output	Stat bit 3 to DSP Interf boards	0	CH-adapter board
	1	S1	Output	Stat bit 3 to DSP Interf boards	0	CH-adapter board
	0	S0	Output	Stat bit 3 to DSP Interf boards	0	CH-adapter board
	PatchClk *)	O, pos. pulse	Strobes the RC register, on CH-adapter board			
	LPRPint		Last Pulse Repetition Period interrupt from R/C HW			
	PatchClk		Timing Clock, from R/C HW			
	Running		R/C running, from R/C HW			

1) Pins are defined in the RC pin assignment document (rows P2 a1-a32 and P2 c1- c32).
 *) Separate signal from the RC.

O =output, BM = Buffer Memory

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