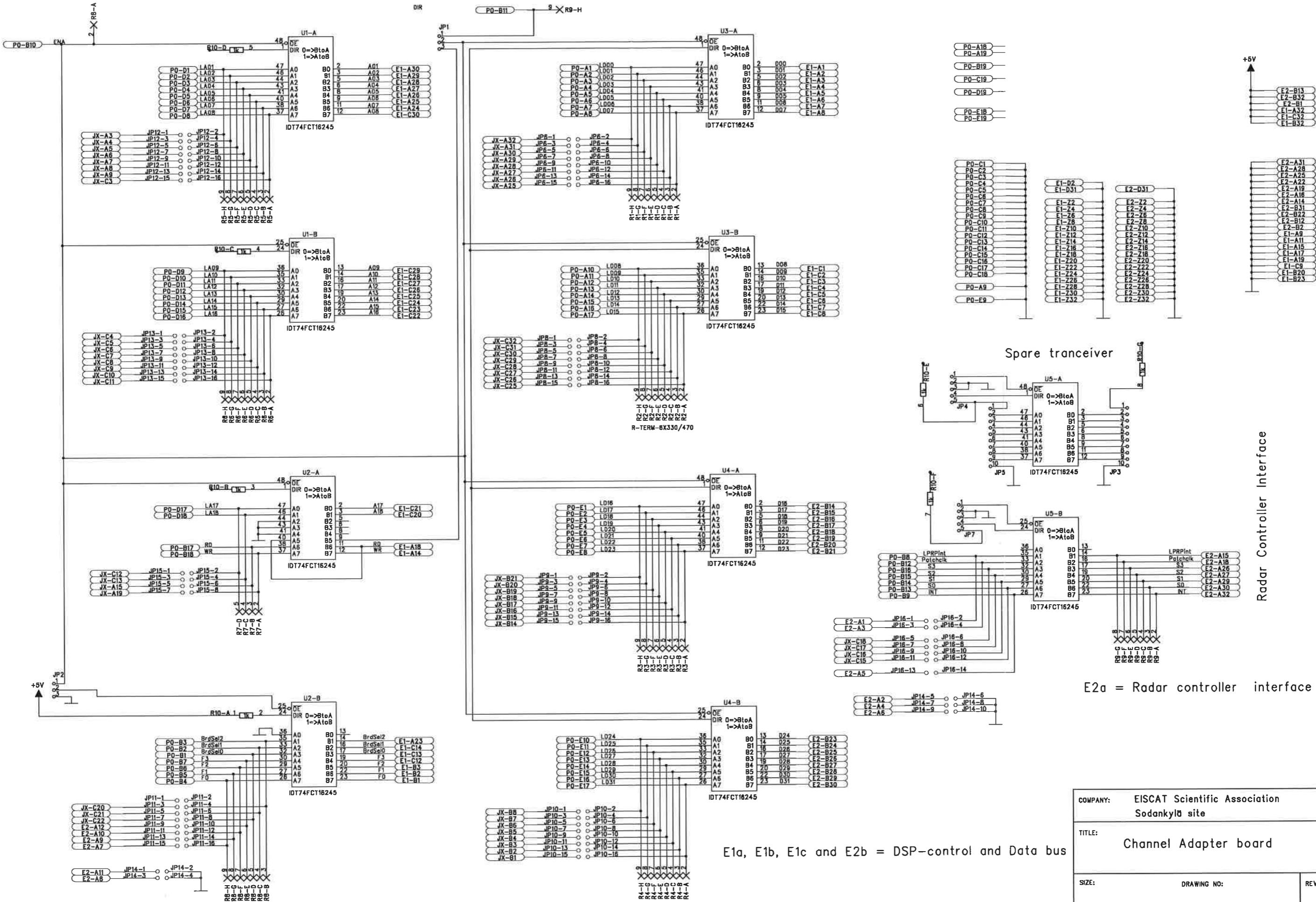


6

5

1



P0 = intermediate connector between the VME interface board and the Channel adapter board

E1a, E1b, E1c and E2b = DSP-control and Data bus

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TITLE: Channel Adapter board

SIZE:	DRAWING NO:	REV:
Layout versien 0		
DRAWN: MJP/TI	DATED: 3.12.1999	MODIFIED: mjp/ti DATED: 6.6.2000
CHECKED:	DATED:	SHEET: 1 OF 2

6

5

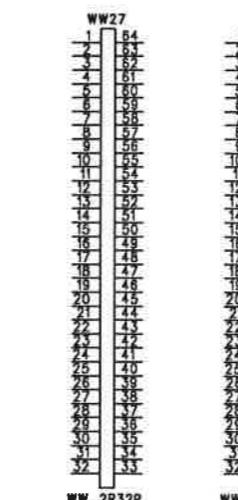
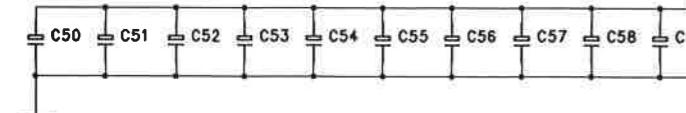
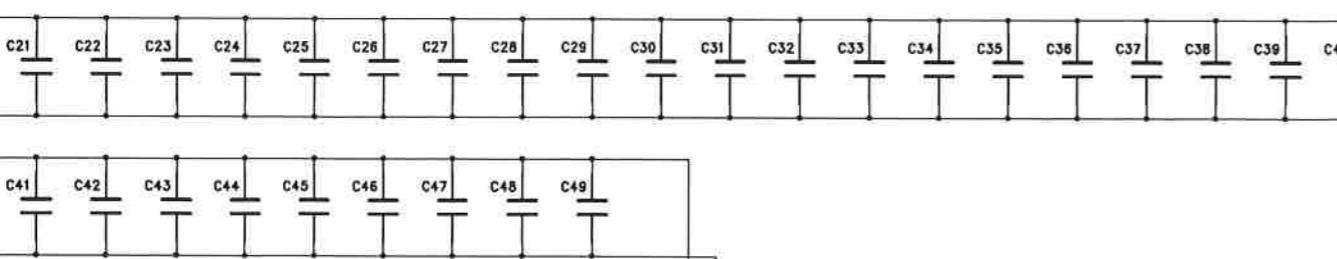
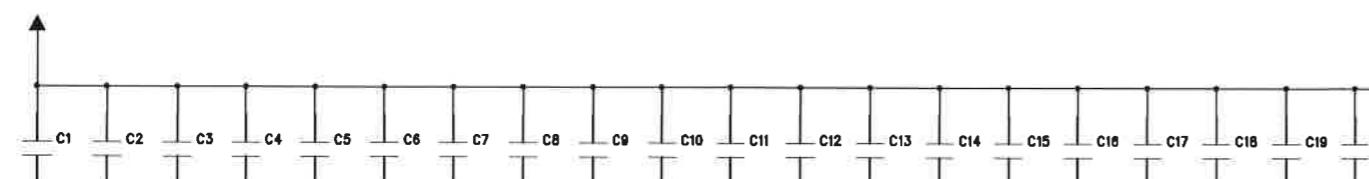
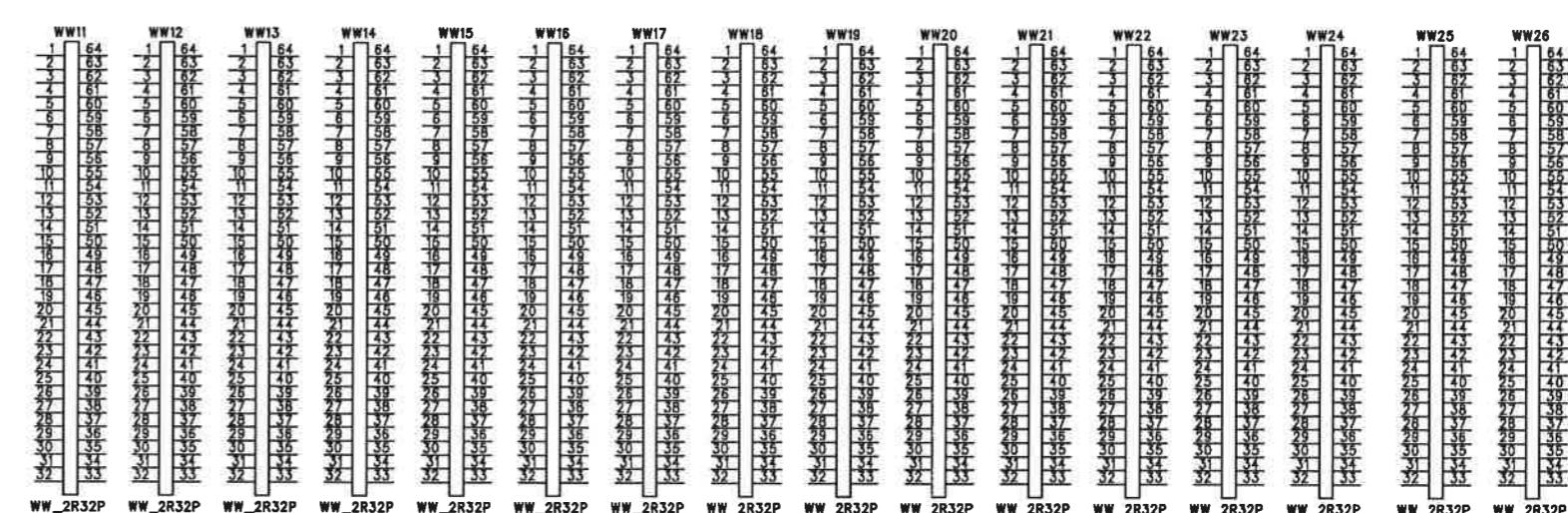
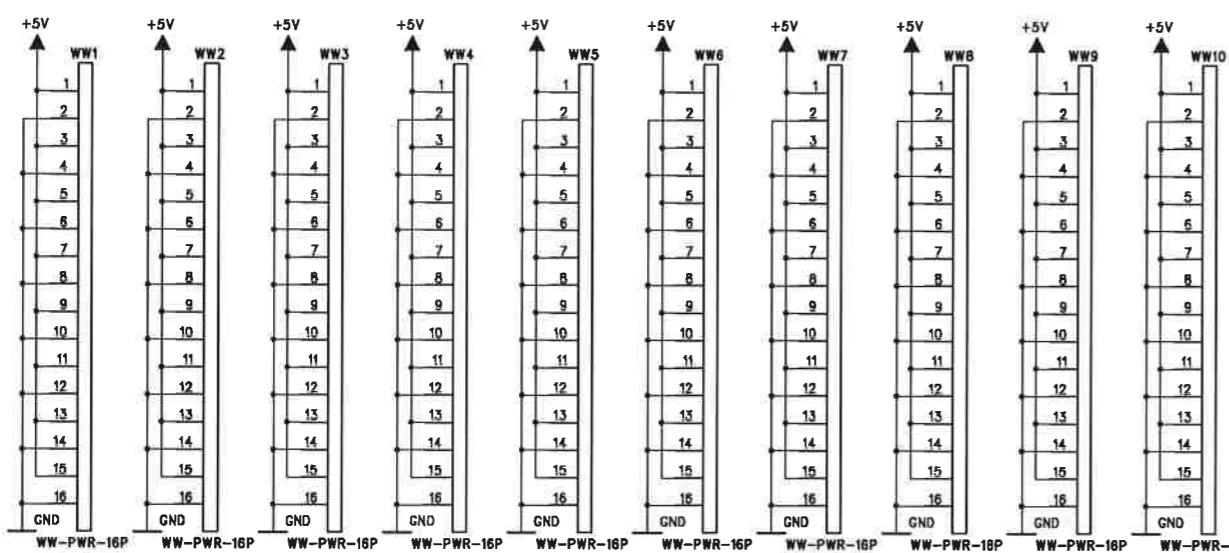
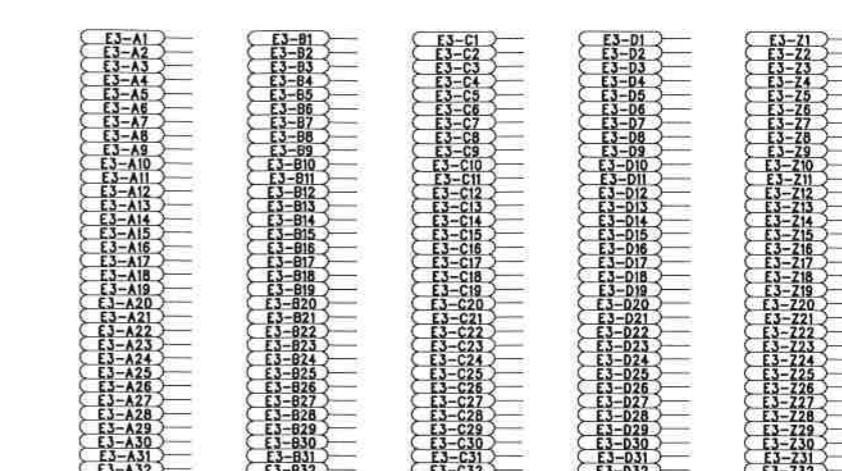
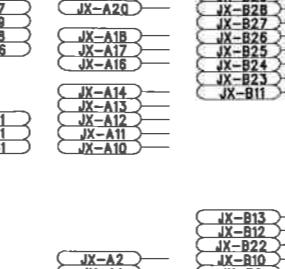
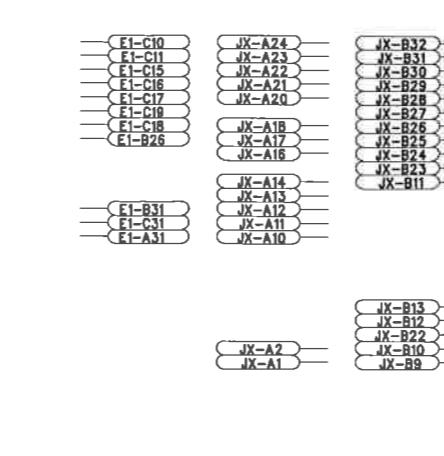
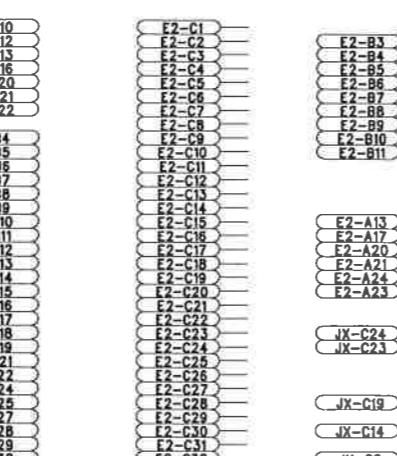
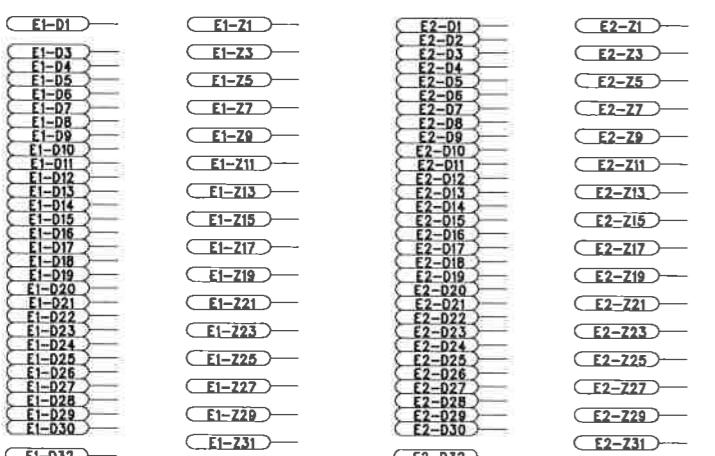
4

3

2

1

D



- JP17-1
- JP17-2
- JP17-3
- JP17-4
- JP17-5
- JP17-6
- JP17-7
- JP17-8
- JP17-9
- JP17-10
- JP17-11
- JP17-12
- JP17-13
- JP17-14
- JP17-15
- JP17-16



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TITLE:

SIZE: DRAWING NO: REV:

DRAWN: DATED: MODIFIED: DATED: 6.6.2000

CHECKED: DATED: SHEET: OF

A

D

C

B

A