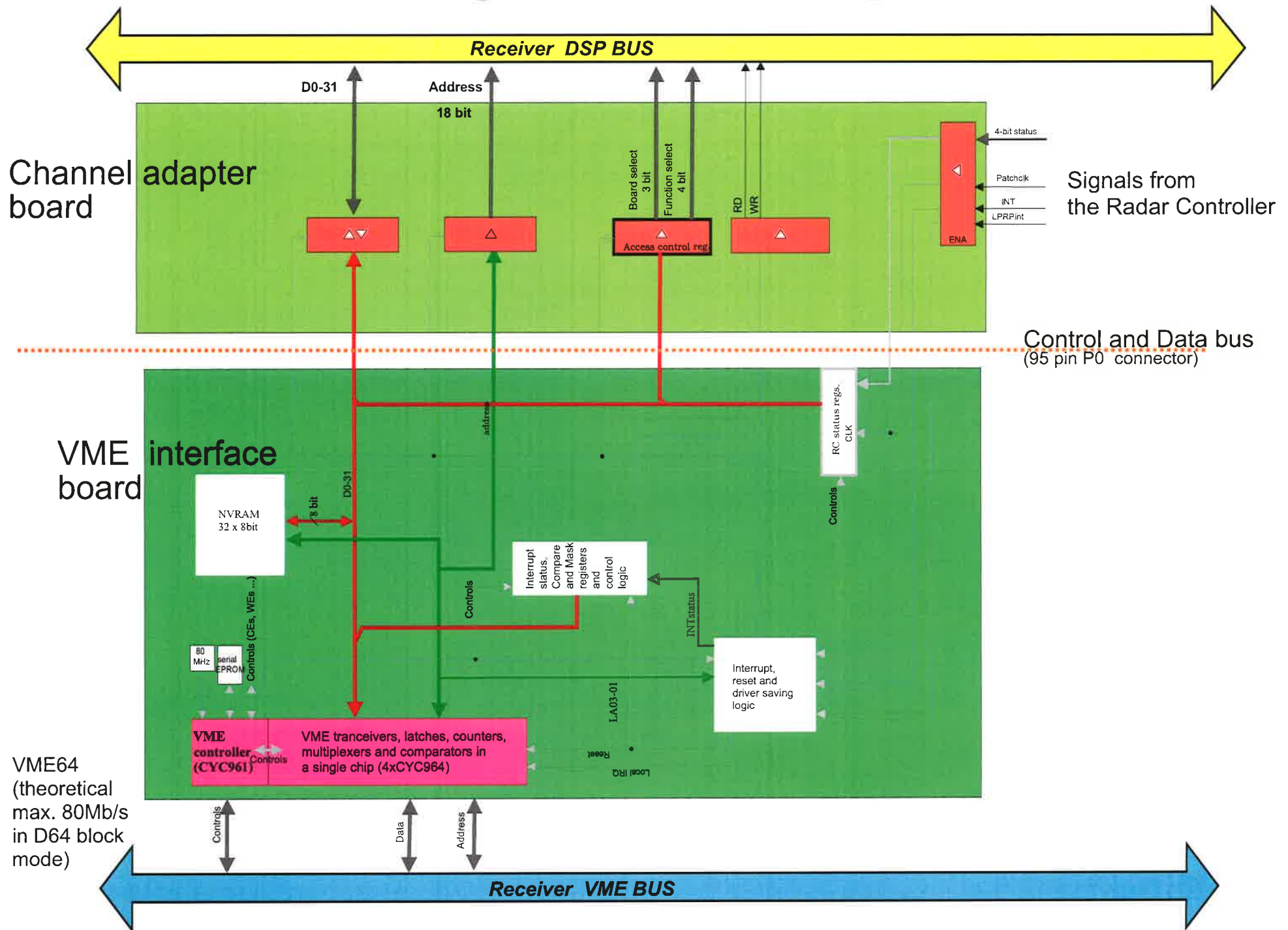
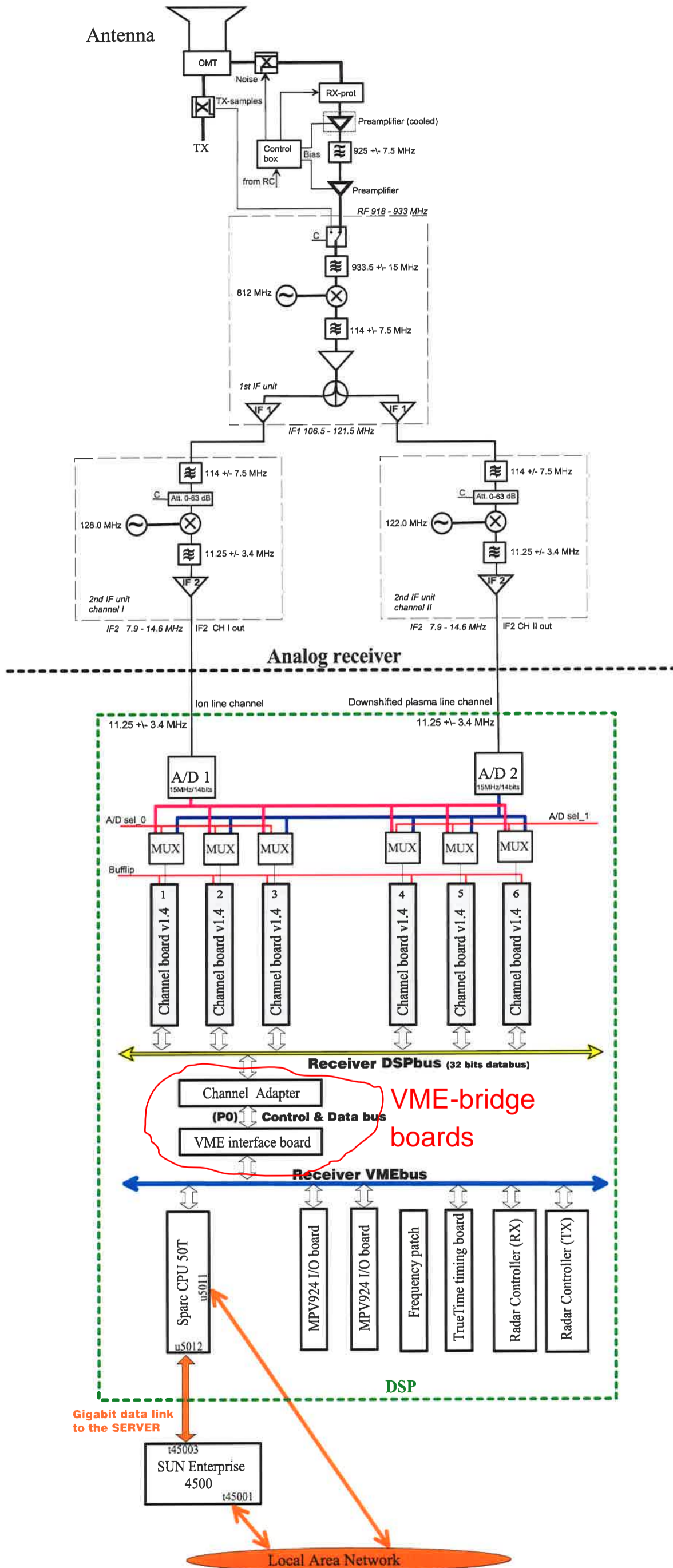
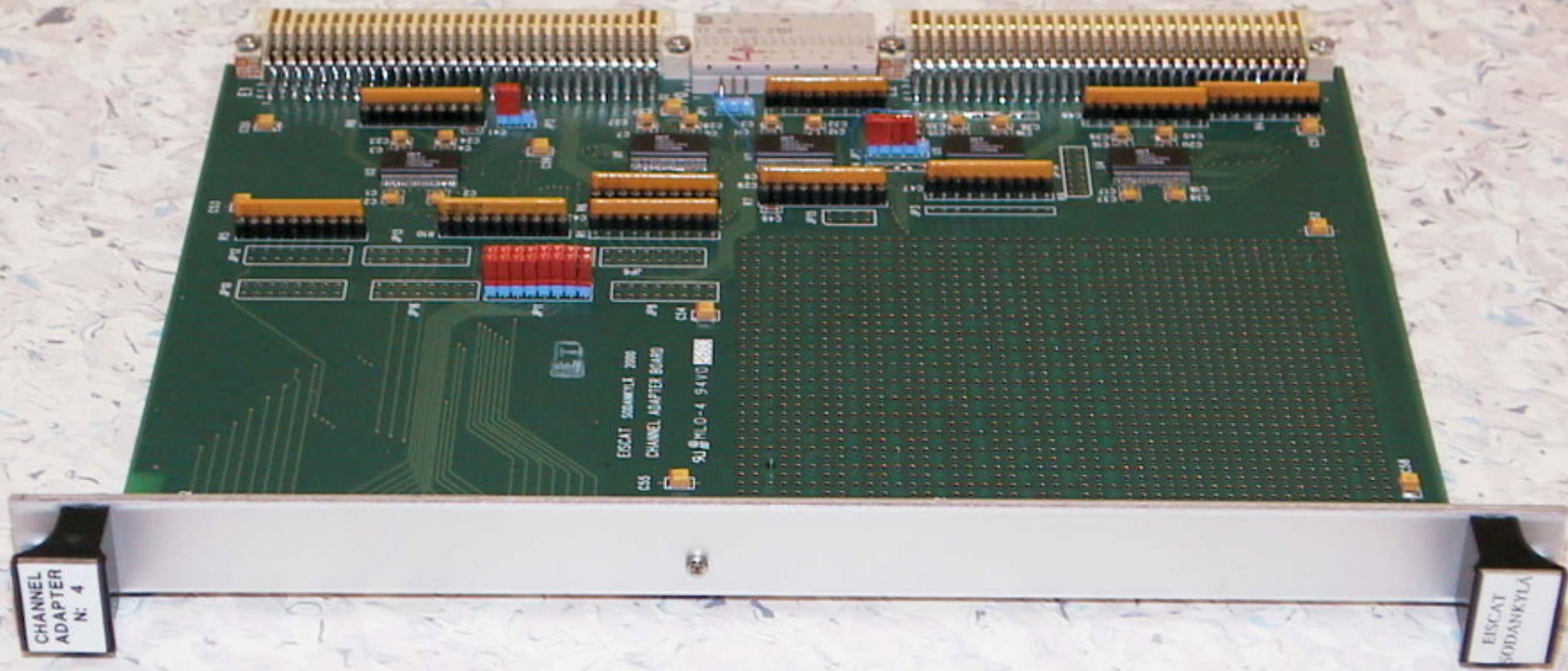


# VME BRIDGE



# UHF RECEIVER (Tromsø site, simplified block diagram)

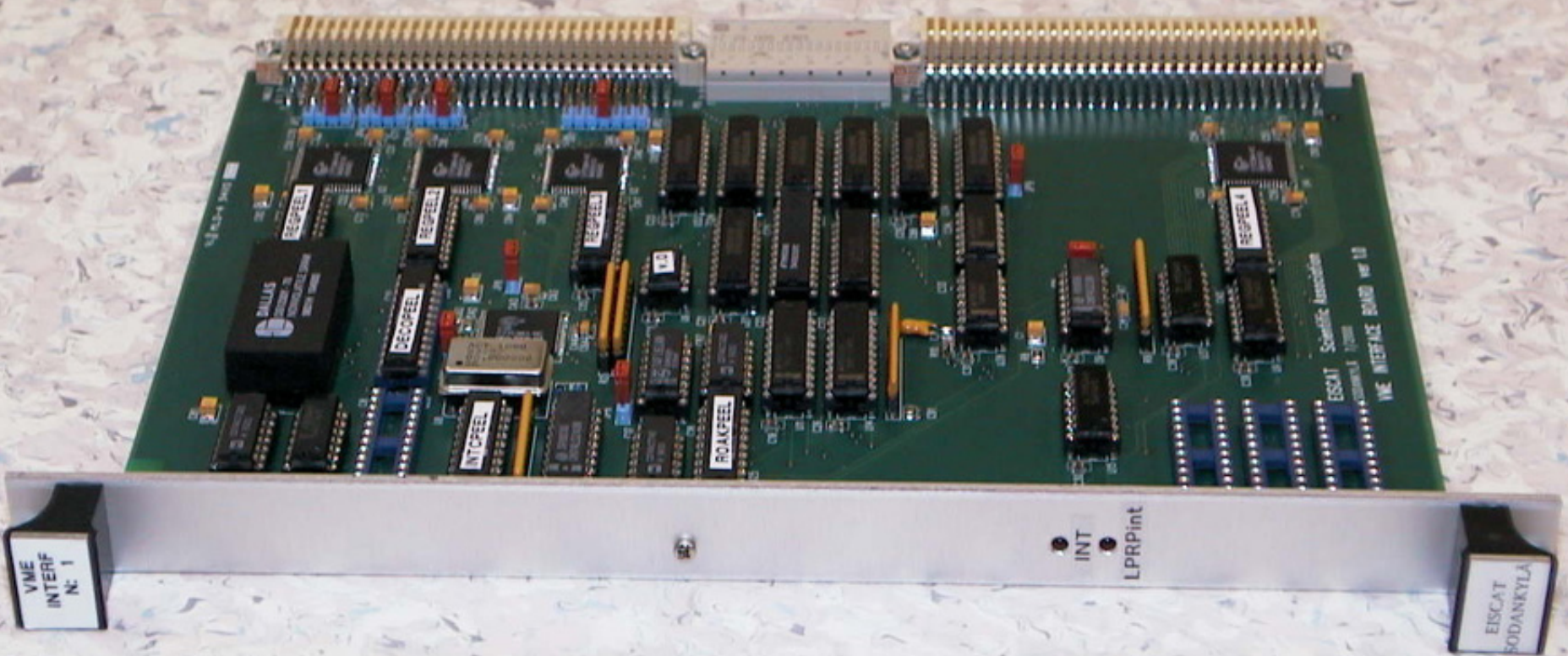




ESCAT 2000KYLÄ 2000  
CS5 CHANNEL ADAPTER BOARD  
SU M.C.O.-4 9410

CHANNEL  
ADAPTER  
N: 4

ESCAT  
2000KYLÄ



VME-4 100

DALLAS  
SEMICONDUCTOR  
CORPORATION  
DALLAS, TEXAS

28C01PEEL

28C01PEEL

28C01PEEL

28C01PEEL

ROAKPEEL

28C01PEEL

EISCAT Scientific Association  
EISCAT SODANKYLÄ  
FINLAND

VME INTERFACE BOARD ver 1.0

VME  
INTERF  
N: 1

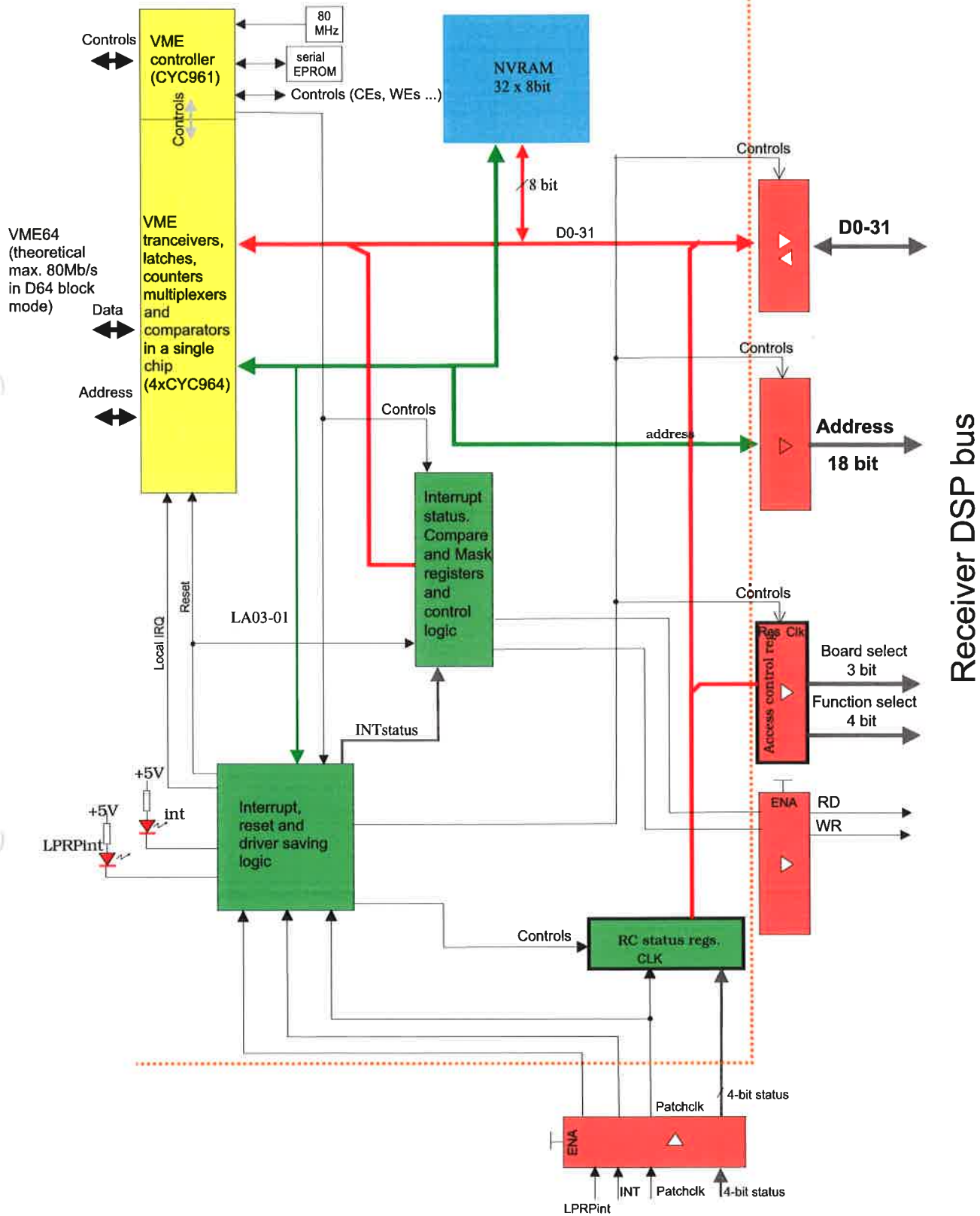
● INT ●  
LPRPrint

EISCAT  
SODANKYLÄ

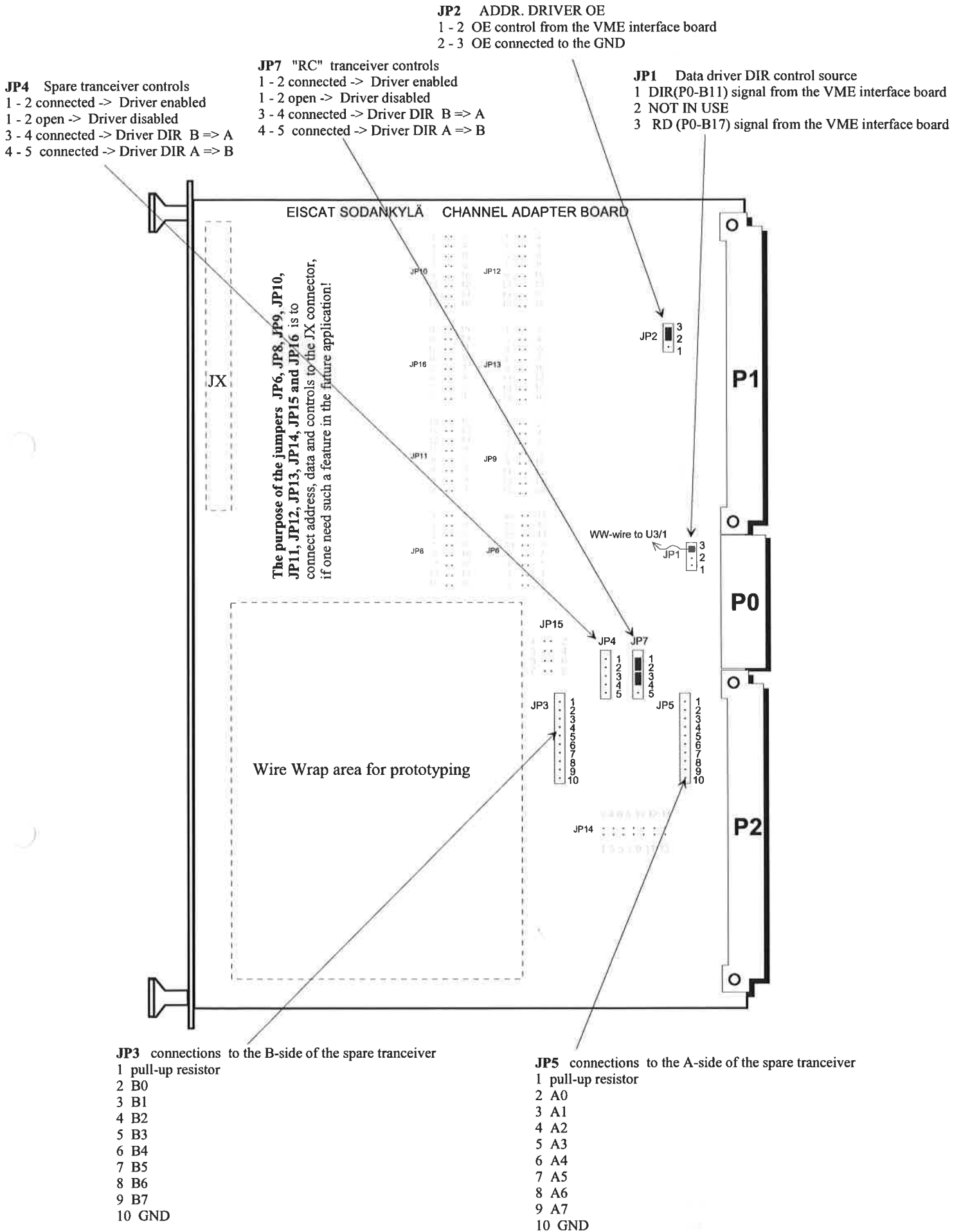
VME interface

VME interface board simplified block diagram

Channel adapter board



From Radar Controller



**JP1** Bus Request level - only one jumper is connected at the time (necessary only, if the board is planned to use as a bus master)  
 1 - 2 BR0 level selected  
 3 - 4 BR1 level selected  
 5 - 6 BR2 level selected  
 7 - 8 BR3 level selected

**JP4** Bus Grant Out - only one jumper is connected at the time  
 1 - 2 BGOUT0 selected  
 3 - 4 BGOUT1 selected  
 5 - 6 BGOUT2 selected  
 7 - 8 BGOUT3 selected

**JP3** Bus Grant In - only one jumper is connected at the time  
 1 - 2 BGIN0 selected  
 3 - 4 BGIN1 selected  
 5 - 6 BGIN2 selected  
 7 - 8 BGIN3 selected

**JP2** Interrupt level select - only one jumper is connected at the time  
 1 - 2 IRQ1 selected  
 3 - 4 IRQ2 selected  
 5 - 6 IRQ3 selected  
 7 - 8 IRQ4 selected  
 9-10 IRQ5 selected  
 11-12 IRQ6 selected  
 13-14 IRQ7 selected

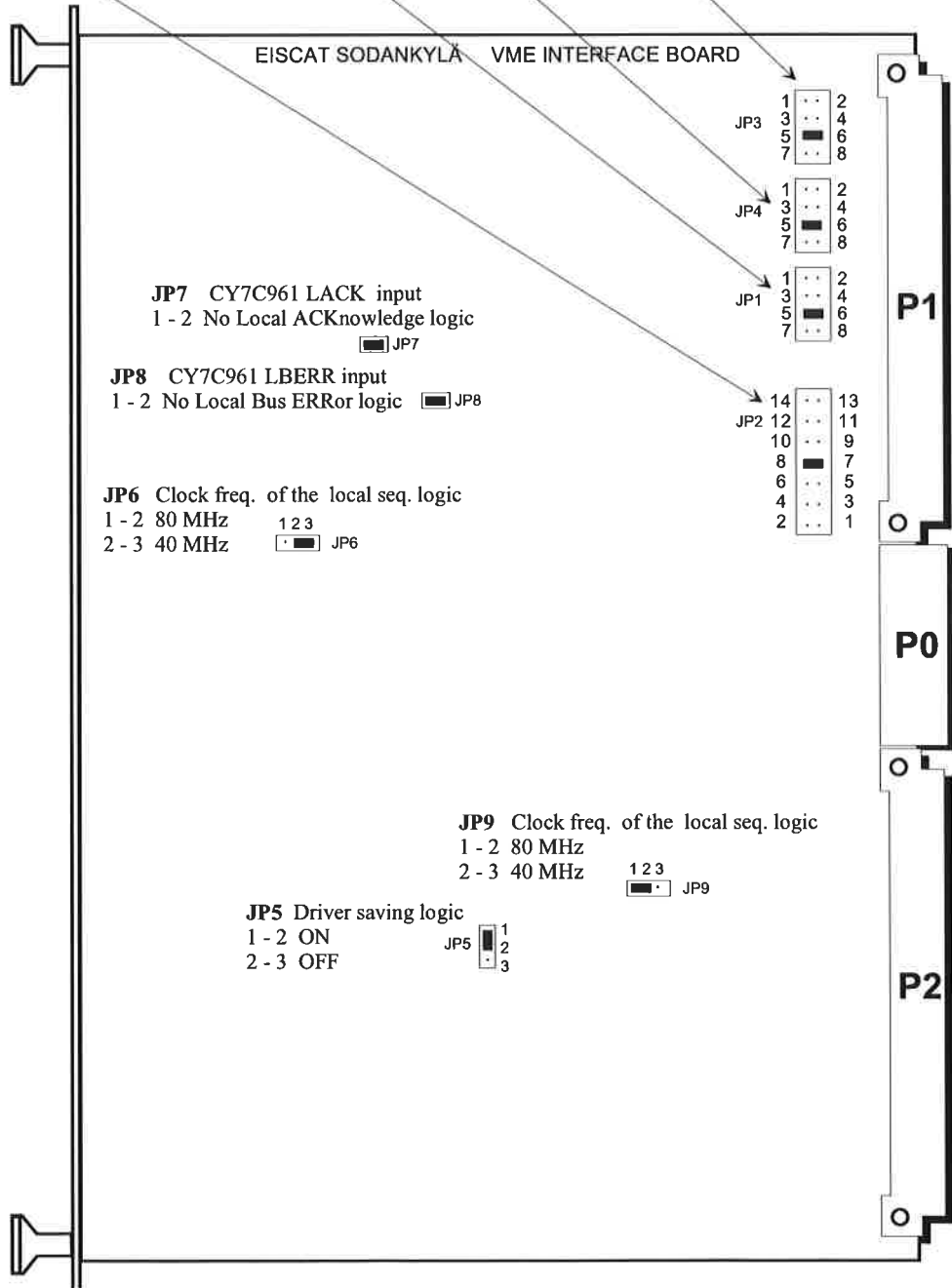
**JP7** CY7C961 LACK input  
 1 - 2 No Local ACKnowledge logic  JP7

**JP8** CY7C961 LBERR input  
 1 - 2 No Local Bus ERRor logic  JP8

**JP6** Clock freq. of the local seq. logic  
 1 - 2 80 MHz  JP6  
 2 - 3 40 MHz

**JP9** Clock freq. of the local seq. logic  
 1 - 2 80 MHz  JP9  
 2 - 3 40 MHz

**JP5** Driver saving logic  
 1 - 2 ON  JP5  
 2 - 3 OFF



# Board select and driver saving logic principle

